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... Network Programs has worked with Japan based company's **ATM** switch software team in the areas of Call ... the collected data crosses the **stored threshold** value then a warning signal ...  
[more hits from: http://www.networkprograms.com/subc/Html/Focusareas/Embed\\_technology\\_link3.htm](http://www.networkprograms.com/subc/Html/Focusareas/Embed_technology_link3.htm) - 19 KB

**Jun. 14, 02 Press**

LIGHTech Fiberoptics, Inc. ... and secondary paths are monitored and compared to a preset **stored threshold**. When the power level in the primary link ... Signal path protection for **ATM**, IP, and DWDM systems ...  
[more hits from: http://www.lightech.net/prs061402.htm](http://www.lightech.net/prs061402.htm) - 9 KB

**..... ams TECHNOLOGIES PRODUCTS :.....**

... path protection for asynchronous transfer mode (**ATM**), Internet protocol (IP), and dense wavelength division ... then compares these levels to a **stored threshold**. When the primary link ...  
[more hits from: http://www.ams.de/en/products/index.php?c=pdetail&id=498](http://www.ams.de/en/products/index.php?c=pdetail&id=498) - 30 KB

**Credit card system and method**

... limited credit access to an account at an **ATM**. These patents disclose a currency ... if the recent transaction exceeds some **stored threshold** limit. These threshold limits can be stored ...  
[more hits from: http://patent.tange.dk/ziki/EP/1/0/EP1029311.html](http://patent.tange.dk/ziki/EP/1/0/EP1029311.html) - 166 KB

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IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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### 1 [Evolutionary computation and optimization \(ECO\): Immunity-based intrusion detection system design, vulnerability analysis, and GENERTIA's genetic arms race](#)



Haiyu Hou, Gerry Dozier

 March 2005 **Proceedings of the 2005 ACM symposium on Applied computing**

Full text available: pdf(94.93 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The GENERTIA red team uses a genetic algorithm to perform vulnerability analysis in an effort to discover holes in an intrusion detection system. This paper demonstrates how a GENERTIA blue team generates detectors to patch those holes discovered by the GRT. The GBT uses a novel approach to generate detectors that have different coverage in order to reduce the amount of overlap of detectors created by random generation and negative selection.

**Keywords:** AIS, AIS redesign, GENERTIA, genetic algorithm, vulnerability analysis

### 2 [Session 4B: Software engineering I: Software engineering process support based on fuzzy imploding Petri nets](#)



Richard T. Bechtold

 April 1992 **Proceedings of the 30th annual Southeast regional conference**

Full text available: pdf(595.92 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#)

Software engineering process management and control is increasingly recognized as critical to the timely engineering of quality products. Automated process support within an integrated environment provides otherwise unachievable levels of insight, coordination, control, and flexibility in governing the activities and resources that comprise the engineering process. Crucial to process flexibility and robustness is the ability of a process to both withstand and react to the impact of unexpected or ...

### 3 [Hoard: a scalable memory allocator for multithreaded applications](#)



Emery D. Berger, Kathryn S. McKinley, Robert D. Blumofe, Paul R. Wilson

 November 2000 **Proceedings of the ninth international conference on Architectural support for programming languages and operating systems**, Volume 28 , 34 Issue 5 , 5

Full text available: pdf(430.66 KB)

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Parallel, multithreaded C and C++ programs such as web servers, database managers, news servers, and scientific applications are becoming increasingly prevalent. For these

applications, the memory allocator is often a bottleneck that severely limits program performance and scalability on multiprocessor systems. Previous allocators suffer from problems that include poor performance and scalability, and heap organizations that introduce false sharing. Worse, many allocators exhibit a dramatic incr ...

#### 4 Hoard: a scalable memory allocator for multithreaded applications



Emery D. Berger, Kathryn S. McKinley, Robert D. Blumofe, Paul R. Wilson

November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Full text available:  pdf(1.47 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Parallel, multithreaded C and C++ programs such as web servers, database managers, news servers, and scientific applications are becoming increasingly prevalent. For these applications, the memory allocator is often a bottleneck that severely limits program performance and scalability on multiprocessor systems. Previous allocators suffer from problems that include poor performance and scalability, and heap organizations that introduce false sharing. Worse, many allocators exhibit a dramatic incr ...

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1 - 10 of 18 Results for "release threshold" "ATM"

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... Canadian CU outsources its **ATM** program to Threshold Electronics ... financial transaction switching and **ATM** management services. According to a news **release**, **Threshold** and its partner ...  
[more hits from:](#) [http://www.atmmarketplace.com/news\\_printable.htm?id=12408](http://www.atmmarketplace.com/news_printable.htm?id=12408) - 2 KB

[Canadian CU outsources its \*\*ATM\*\* program to Threshold Electronics | \*\*ATM\*\* Marketplace News](#)

... transaction switching and **ATM** management services. According to a news **release**, **Threshold** and its partner ... **ATM** Components & Technology ( **ATM** Exchange **ATM** Express **ATM** Express, Inc **ATM** ...  
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[Drupal 4.6.0 release candidate | drupal.org](#)

Drupal 4.6.0 release candidate. Dries - March 5, 2005 - 14:00. We are pleased to announce that the Drupal 4.6.0 release candidate is available. A list of the major changes can be found in the CHANGELOG file.

[more hits from:](#) <http://drupal.org/drupal-4.6.0-rc> - 206 KB

[Method for protecting a network from data packet overload - US Patent 6189035](#)

... packet count for a source address is below a **release threshold**, the source address is removed from the address table ... 5914936. **ATM** exchange performing traffic flow control ...

[more hits from:](#) <http://www.patentstorm.us/patents/6189035.html> - 31 KB

[The State House](#)

The State House, 2001, as it happens. For headlines running from Jan. 2, 2001 to Mar. 30, 2001, click [here](#). For headlines running from Apr. 1 to June 30, 2001, click [here](#). For headlines running from July 1 to Sept. 30, 2001, click [here](#). MONDAY, OCT.

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[Sound Tool - Cool Audio Extractor Tool Spanish Whiz - Pocket Voice Recorder](#)

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[Hotscripts.com :: Search](#)

... A/B - Waterproof: 3~10 **ATM** - Indicator Light: Yellow/Red/Green - Equalizer ... Compressor: gain, attack, **release**, **threshold**, ratio, predelay. Distortion: gain, edge, postEQ center ...

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[Human Rights and Equal Opportunity Commission Website: Disability Rights](#)

... industrial data services include financial transactions (**ATM** and EFTPOS), computer network traffic, and

broadcasting ...

more hits from: [http://www.hreoc.gov.au/disability\\_rights/communications/tide2.htm](http://www.hreoc.gov.au/disability_rights/communications/tide2.htm) - 111 KB

<http://www.rockbox.org/irc/rockbox-20031231.txt>

Background: it might be nice to toggle prerecording in the f2 screen. Keep the time, just toggle it on and off. ... stupid or does the digital in not work atm? 02.41.12 # <LinusN> i really hope you are ... attack duration, attack dropout and **release threshold**, release hold 04.23.40 ...

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(L49 AND TEAR NEAR DOWN NEAR THRESHOLD ).PGPB,USPT.	0

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*DB=PGPB,USPT; PLUR=YES; OP=ADJ*

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0   L51

L50   L49 and release near threshold

1   L50

L49   L48 and capacity and setup

21   L49

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<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L9</u>	370/395.210.ccls.	0	<u>L9</u>



<u>L8</u>	370/395.ccls.	0	<u>L8</u>
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<u>L1</u>	ATM and setup near threshold	5	<u>L1</u>

END OF SEARCH HISTORY